

L&T Press Release

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LTSCT and Andes Technology Sign Strategic IP Licensing Master Agreement to accelerate RISC-V-Based Advanced Semiconductor Solutions

Mumbai, 12 January 2026 — LTSCT, a wholly owned subsidiary of Larsen & Toubro (L&T), has announced an IP License Master Agreement with Andes Technology, a leading provider of RISC-V Processor IPs. The agreement establishes a scalable framework for long-term intellectual property (IP) collaboration and innovation, enabling LTSCT to accelerate the development and commercialisation of RISC-V-based advanced, customised semiconductor solutions for global markets.

Under the terms of the agreement, key areas of alignment include the scope of intellectual property licensing, a cumulative financial structure that supports expanded modification rights, and standard legal provisions addressing confidentiality and technical support. Together, these elements are designed to provide clarity, flexibility, and scalability as both companies pursue long-term growth and innovation together.

The IP License Master Agreement is designed with three primary objectives. First, it establishes a comprehensive legal framework to govern both current and future licensing transactions between the parties. Second, it grants LTSCT the rights required to incorporate Andes' RISC-V technology into LTSCT's products, thereby accelerating development and commercialisation efforts. Third, the agreement introduces a structured, cumulative business model that outlines a clear progression toward broader modification rights for LTSCT as the partnership evolves.

Commenting on the Master Agreement, **Mr Sandeep Kumar, Chief Executive, LTSCT**, said: “This agreement lays the foundation for a strategic, long-term partnership anchored in shared priorities around innovation, operational efficiency, and disciplined execution. By pairing a clear, scalable legal framework with a forward-looking business model, we enable faster collaboration, more predictable IP deployment, and improved alignment between technology development and commercial outcomes. This structure positions both LTSCT and Andes to deliver differentiated solutions, strengthen customer value propositions, and compete more effectively in an increasingly complex semiconductor market”.

Mr Frankwell Jyh-Ming Lin, CEO, Andes Technology, added: “We are pleased to have reached an agreement with LTSCT, which establishes a clear and scalable framework for long-term collaboration. By aligning our IP assets with LTSCT’s product and market strategy, the agreement enables faster execution, greater flexibility, and expanded commercial opportunities, ultimately delivering increased value across the semiconductor ecosystem”.

The Agreement is expected to deliver meaningful benefits across the semiconductor ecosystem and for end customers. By accelerating LTSCT’s ability to develop, customise, and commercialise advanced semiconductor devices using cutting-edge IP, the collaboration aims to drive innovation, shorten time-to-market, and potentially reduce overall development and production costs.

About L&T Semiconductor Technologies (LTSCT):

LTSCT, a fully owned subsidiary of L&T, is the first major Indian Semiconductor product company - a fabless company for designing & delivering Smart Devices for Global Customers. A company that provides Semiconductor Devices and Technology partnerships by helping customers realise energy-efficient, high-performance systems to benefit from data, electrification and software-defined technology trends. Our aim is to build an India-based semiconductor portfolio of Smart Devices across MEMS Sensors, Power, Analog Mixed Signal & RF products, to support Automotive, Industrial, Energy and Telecommunication verticals. We have a presence in four prominent geographies, i.e. US, Europe, Japan and India, with offices in Austin, Munich, Tokyo, Bangalore and Chennai.

For more information, visit <https://www.ltsct.com/about-us/>

About Andes Technology:

As a Founding Premier member of RISC-V International and a leader in commercial CPU IP, Andes Technology ([TWSE: 6533](#); [SIN: US03420C2089](#); [ISIN: US03420C1099](#)) is driving the global adoption of RISC-V. Andes’ extensive RISC-V Processor IP portfolio spans from ultra-efficient 32-bit CPUs to high-performance 64-bit Out-of-Order multiprocessor coherent clusters. With advanced vector processing, DSP capabilities, the powerful Andes Automated Custom Extension (ACE) framework, end-to-end AI hardware/software stack, ISO 26262 certification with full compliance, and a robust software ecosystem, Andes unlocks the full potential of RISC-V, empowering customers to accelerate innovation across AI, automotive, communications, consumer electronics, data centers, and mobile devices. Over 17 billion Andes-powered SoCs are driving innovations globally. Discover more at www.andestech.com and connect with Andes on [LinkedIn](#), [X \(formerly Twitter\)](#), [YouTube](#) and [Bilibili](#).

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